

Lab 02: EDA - Schematic Capture

- Create a new project and re-create the schematic shown below.
- Incorporate the following improvements:
 - Ground should be a power port (instead of a global net label).
 - A voltage power port for the battery voltage net.
 - The AND gate should use a common logical symbol instead of the annotated block.
- Make sure that your schematic passes ERC.
- Submit a PDF of your schematic and a screenshot of your passed ERC.

